

Center for Microelectronics in Extreme Environments (CMEE) **Guidelines for Cleanroom Space Usage**

The current cleanroom is a combination of mostly 10,000 and some 100-space occupying only 3000 sq. ft. The purposes of the CMEE cleanroom are two-fold:

1. Provide workforce development (WFD) and training facilities for the North Texas Semiconductor sector; and
2. Support on-going and future semiconductor-related research efforts with accessible fabrication tools and instrumentation.

To fulfill its purpose and ensure that both goals are met, the following guidelines apply:

1. Space priority will be given to equipment that can be operated **only** in a cleanroom.
2. Space priority will be given to semiconductor and nanoscale **processing** equipment.
3. To the highest degree possible, cleanroom equipment will **simultaneously** satisfy WFD, research, and manufacturing support needs.
4. Single user or occasional user equipment may be removed if space requirements conflict with multi-user processing equipment or other WFD needs.
5. Out of order equipment that cannot be affordably repaired will be removed as soon as feasible to free up valuable cleanroom space.
6. The CMEE cleanroom committee in consultation with the Office of the VPRI will determine the most efficient use of space to fulfill the cleanroom's functions.